

I 04 ELECTRONICS & TELECOMM.
COLLEGE : GOA COLLEGE OF ENGINEERING** RC 2007-08
EXAM YEAR : NOV-2014SEAT P.R. NO. ARE PRO SEX NAME OF THE CANDIDATE
NO. CD. VI.

SEAT P.R. NO.	ARE PRO	SEX	NAME OF THE CANDIDATE				SEM. 6	SEM. 5	GRAND		
NO.	CD.	VI.					TOTAL	TOTAL	TOTAL		
				THEORY	SESS	TOTAL	MAX. 850	MAX. 850	MAX. 1700		
				MAX. 100	25	125	MIN. 340	MIN. 340	MIN. 0680		
							PRACTICAL	ORAL		REMARKS	
							50 OR 25	50 OR 25			
0047	201105588	(E)	N	M	CHIBDE GAUTAM ULHAS						
					COMMUNICATION ENGINEERING II	057	010	067+		033+	
					PERIPHERAL DEVICES & INTERFACING	046	009	055P			
					POWER ELECTRONICS	047	012	059+			
					ANTENA & WAVE PROPAGATION	040	014	054+			
					ELECTRONIC INSTRUMENTATION	049	010	059+			
					VLSI TECHNOLOGY & DESIGN	041	013	054+			
									409	026+	
									P	436	\$05 P
										0845	\$05 C
0048	201104821	(E)	N	M	DHURI RAMA SHANKAR						
					COMMUNICATION ENGINEERING II	039	\$1 006	045+	\$5	035+	
					PERIPHERAL DEVICES & INTERFACING	045	009	054P			
					POWER ELECTRONICS	041	008	049+	\$1		
					ANTENA & WAVE PROPAGATION	050	014	064+			
					ELECTRONIC INSTRUMENTATION	035	\$5 010	045+	\$5		
					VLSI TECHNOLOGY & DESIGN	053	005	058+			
										026+	
									376	\$11 P	410
										\$01 C	0786
											\$12 C
0049	200801315	(E)	N	M	GAONKAR KISAN DHAKALI					** RESULT WITHHELD - (SEM. 5 NOT PASSED) **	
					COMMUNICATION ENGINEERING II	040	004	044+	\$6	035+	
					PERIPHERAL DEVICES & INTERFACING	000	000	000A			
					POWER ELECTRONICS	040	005	045+	\$5		
					ANTENA & WAVE PROPAGATION	053	004	057E			
					ELECTRONIC INSTRUMENTATION	000	000	000A			
					VLSI TECHNOLOGY & DESIGN	021	003	024F		026+	
0050	201104825	(E)	N	M	JOSHI ADITYA KALIDAS					** RESULT WITHHELD - (SEM. 5 NOT PASSED) **	
					COMMUNICATION ENGINEERING II	000	006	006A		000A	
					PERIPHERAL DEVICES & INTERFACING	000	000	000A			
					POWER ELECTRONICS	019	017	036F			
					ANTENA & WAVE PROPAGATION	035	\$5 014	049+	\$5		
					ELECTRONIC INSTRUMENTATION	000	011	011A			
					VLSI TECHNOLOGY & DESIGN	000	015	015A		000A	
0051	201008572	(E)	N	M	K. J. DEEPAK					** RESULT WITHHELD - (SEM. 5 NOT PASSED) **	
					COMMUNICATION ENGINEERING II	060	004	064+		038+	
					PERIPHERAL DEVICES & INTERFACING	042	010	052+			
					POWER ELECTRONICS	040	008	048E	\$2		
					ANTENA & WAVE PROPAGATION	000	007	007A			
					ELECTRONIC INSTRUMENTATION	047	009	056+			
					VLSI TECHNOLOGY & DESIGN	060	005	065+		026+	

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SEAT NO.	P.R. NO.	ARE PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESS	TOTAL	SEM. 6 TOTAL MAX. 850 MIN. 340 PRACTICAL 50 OR 25	SEM. 5 TOTAL MAX. 850 MIN. 340 ORAL 50 OR 25	GRAND TOTAL MAX. 1700 MIN. 0680	REMARKS
NO.				CD. VI.	MAX. 100	25	125				
** RESULT WITHHELD - (SEM. 5 NOT PASSED) **											
0052	200907860	(E)	N	F	KAMBLI KENAUDEKAR KUMARSEN TWINKLE	000	000	000A		020+	
					COMMUNICATION ENGINEERING II	000	001	001A			
					PERIPHERAL DEVICES & INTERFACING	050	013	063+			
					POWER ELECTRONICS	045	010	055+			
					ANTENA & WAVE PROPAGATION	035	#5 010	045+	#5		
					ELECTRONIC INSTRUMENTATION	049	010	059+		032+	
					VLSI TECHNOLOGY & DESIGN						
** RESULT WITHHELD - (SEM. 5 NOT PASSED) **											
0053	201008341	(E)	N	M	KESHAV KAUSTUBH LAXMIKANT BHORE	051	008	059+		037+	
					COMMUNICATION ENGINEERING II	035	005	040F			
					PERIPHERAL DEVICES & INTERFACING	055	015	070+			
					POWER ELECTRONICS	038	#2 013	051+	#2		
					ANTENA & WAVE PROPAGATION	037	#3 013	050+	#3		
					ELECTRONIC INSTRUMENTATION	048	014	062+		034+	
					VLSI TECHNOLOGY & DESIGN				403	#05 F 429	#05 C 0832
											#10 F
** RESULT WITHHELD - (SEM. 5 NOT PASSED) **											
0054	201105612	(E)	N	M	KHARANGATE VIBHAV VAMAN	035	#5 011	046E	#5	035+	
					COMMUNICATION ENGINEERING II	000	013	013A			
					PERIPHERAL DEVICES & INTERFACING	040	011	051+			
					POWER ELECTRONICS	035	#5 011	046+	#5		
					ANTENA & WAVE PROPAGATION	035	#5 010	045+	#5		
					ELECTRONIC INSTRUMENTATION	060	015	075+		035+	
					VLSI TECHNOLOGY & DESIGN				346	#15 F 375#09#08 C	0721#09#23 F
** RESULT WITHHELD - (SEM. 5 NOT PASSED) **											
0055	201203909	(E)	N	M	LINGUDKAR YUVRAJ EKNATH	038	#2 010	048+	#2	030+	
					COMMUNICATION ENGINEERING II	035	007	042F			
					PERIPHERAL DEVICES & INTERFACING	041	010	051+			
					POWER ELECTRONICS	061	012	073+			
					ANTENA & WAVE PROPAGATION	035	#5 012	047+	#5		
					ELECTRONIC INSTRUMENTATION	056	010	066+		032+	
					VLSI TECHNOLOGY & DESIGN						
** RESULT WITHHELD - (SEM. 5 NOT PASSED) **											
0056	201008279	(E)	N	M	NAIK VISHNU ALIAS VIRAJ RAMKRISHNA	061	007	068+		032+	
					COMMUNICATION ENGINEERING II	000	005	005A			
					PERIPHERAL DEVICES & INTERFACING	042	011	053+			
					POWER ELECTRONICS	042	010	052+			
					ANTENA & WAVE PROPAGATION	045	011	056+			
					ELECTRONIC INSTRUMENTATION	069	010	079+		037+	
					VLSI TECHNOLOGY & DESIGN						

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THEORY	SESS	TOTAL	SEM. 6		SEM. 5		GRAND TOTAL	REMARKS
			TOTAL	PRACTICAL	TOTAL	ORAL		
MAX. 100	25	125	MAX. 850	50 OR 25	MAX. 850	50 OR 25	MAX. 1700	
			MIN. 340		MIN. 340		MIN. 680	
0057 201006494 (E) N M SAVANT VINAY								
COMMUNICATION ENGINEERING II	000	003	003A			032+		
PERIPHERAL DEVICES & INTERFACING	000	005	005A					
POWER ELECTRONICS	050	011	061+					
ANTENA & WAVE PROPAGATION	000	011	011A					
ELECTRONIC INSTRUMENTATION	036	\$4 009	045+	\$5				
VLSI TECHNOLOGY & DESIGN	056	006	062+					
					249	\$05 A 408	\$15 C	0657 \$20 F
0058 201203914 (E) N M SHIRODKAR NIKHIL PRATAP								
COMMUNICATION ENGINEERING II	000	004	004A			039+		** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
PERIPHERAL DEVICES & INTERFACING	017	013	030F					
POWER ELECTRONICS	052	013	065+					
ANTENA & WAVE PROPAGATION	000	005	005A					
ELECTRONIC INSTRUMENTATION	040	008	048+	\$2				
VLSI TECHNOLOGY & DESIGN	021	010	031F			034+		

READ BY

CHECKED BY

16 MAR 2015

DECLARED ON

ASST. REGISTRAR-E (PROF)

CONTROLLER OF EXAMINATIONS

REGISTRAR